

SUPER I/O CARD USER'S MANUAL

Document Number RD-OP91-12-06-MNU

*ALL PRODUCT AND COMPANY NAMES ARE TRADEMARKS OR
REGISTERED TRADEMARKS OF THEIR RESPECTIVE HOLDERS.*

SPECIFICATIONS SUBJECTS TO CHANGE WITHOUT PRIOR NOTICE.

TABLE OF CONTENTS

<u>SECTION</u>	<u>DESCRIPTIONS</u>	<u>PAGE</u>
1.0	Introduction	1
1.1	Feature	1
1.2	Super I/O Card Layout	2
2.0	Serial Port Selection	3
2.1	Printer Port Selection	4
2.2	IDE Selection	5
2.3	Parallel Port Signal	6
2.4	DP9s to DB25P Adaptor Cable	7
2.5	UM82C862 Chipset Pin Layout	7

1.0 INTRODUCTION

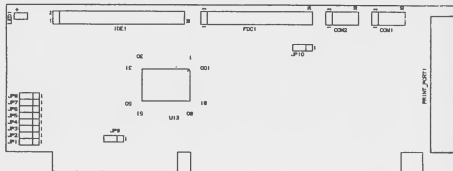
The Super Multi I/O chip, UM82C862 is an integrated chip of 83C88, 82C452, IDE buffer interface and configuration registers. UM82C862 is fully compatible with IBM PC XT/AT. 83C88 is Floppy Disk Controller (FDC) for interfacing up to two floppy disk drives. It is compatible to support 360K, 720K, 1.2M and 1.44M FDD.

82C452 is an enhanced dual channel version of an asynchronous communication element (ACE) and provides Centronics printer interface. A programmable baud rate generator is included that can divide the timing reference clock input by a divisor between 1 and 65535. The I.D.E. port decoding strobes IDE type Winchester drives and supports data buffer. UM82C862 is flexible and easy to select configuration registers. It is also easy to enable or disable any support functions. UM82C862 is packaged in 100 pins plastic flat pack package.

1.1 FEATURES

- * Fully compatible with IBM XT, AT architecture
- * Supports up to two floppy disk drives, all compatible with 360k, 720K, 1.2M or 1.44M drives
- * Supports IBM PC XT/AT compatible board for interfacing to IDE (Intelligent Drive Electronics) type Winchester drives
- * Supports two serial functions
- * Supports one Centronics printer function
- * All functions can be enabled or disabled
- * Configuration Register can be easily selected
- * Only one 24Mhz crystal for FDC and ACE
- * 100 pins plastic flat pack package.

1.2 SUPER I/O CARD LAYOUT



- JP 1 - SELECTION OF PRINTER ADDRESS
- JP 2 - SELECTION OF SERIAL 0 ADDRESS
- JP 3 - SELECTION OF SERIAL 1 ADDRESS
- JP 4 - ENABLE OR DISABLE PRINTER
- JP 5 - ENABLE OR DISABLE SERIAL 0
- JP 6 - ENABLE OR DISABLE SERIAL 1
- JP 7 - ENABLE OR DISABLE HARDDISK
- JP 8 - SELECTION OF HARDDISK ADDRESS
- JP 9 - HARDDISK ABSENT OR PRESENT
- JP 10 - SELECTION OF XT/AT HARDDISK

2.0 SERIAL PORT CONFIGURATION

The Super I/O Card has two standard serial ports. This port can be used to connect the PC-AT to a serial printer, modem, mouse, or other device which use an RS-232C interface,

SELECTION OF SERIAL 0 ADDRESS

JP 2	I/O PORT ADDRESS	COM
1 - 2	3F8 - 3FF	COM 1
2 - 3	3E8 - 3EF	COM 3

SELECTION OF SERIAL 1 ADDRESS

JP 3	I/O PORT ADDRESS	COM
1 - 2	2F8 - 2FF	COM 2
2 - 3	2E8 - 2EF	COM 4

ENABLE OR DISABLE SERIAL 0

JP 5	1 - 2	ENABLE
	2 - 3	DISABLE

ENABLE OR DISABLE SERIAL 1

JP 6	1 - 2	ENABLE
	2 - 3	DISABLE

2.1 PRINTER PORT CONFIGURATION

The Super I/O Card has a standard feature for interfacing the PC-AT a parallel printer such as IBM/EPSON MX-80. This port is completely compatible with the IBM PC-AT and uses the same female DB25 connector as an IBM port. This port may be addressed as either parallel port 1 or 2 by the jumper setting.

SELECTION OF PRINTER ADDRESS

JP 1	I/O PORT ADDRESS	LPT
1 - 2	378 - 37F	LPT 1
2 - 3	278 - 27F	LPT 2

ENABLE OR DISABLE PRINTER

JP 4	1 - 2	ENABLE
	2 - 3	DISABLE

2.2 I.D.E. SELECTION

SELECTION OF HARDDISK ADDRESS

JP 8	I/O PORT ADDRESS	IDE
1 - 2	1F0 - 1F7	IDE 1 (DEFAULT)
2 - 3	170 - 177	IDE 2

ENABLE OR DISABLE HARDDISK

JP 7	1 - 2	ENABLE
	2 - 3	DISABLE

SELECTION OF XT/AT HARDDISK

JP 10	1 - 2	XT MODE
	2 - 3	AT MODE

HARDDISK PRESENT OR ABSENT

JP 9	1 - 2	SYSTEM WITH HDC PRESENT
	2 - 3	SYSTEM WITHOUT HDC PRESENT

2.3 PARALLEL PORT SIGNAL

LINE	DB25S CONNECTOR	IBM MATRIX PRINTER
STROBE	1	1
D0	2	2
D1	3	3
D2	4	4
D3	5	5
D4	6	6
D5	7	7
D6	8	8
D7	9	9
ACK#	10	10
BUSY	11	11
PE	12	12
SLCT	13	13
AUTOFD#	14	14
ERROR#	15	32
INIT#	16	31
SLCT IN#	17	36
GROUND	(18 - 25)	(16,19-30,33)

2.4 DP9S TO DB25P ADAPTOR CABLE

SIGNAL NAME	DB9S CONNECTOR	DB25P CONNECTOR
DCD (DATA CARRIER DETECT)	1	8
RX (RECEIVE DATA)	2	3
TX (TRANSMIT DATA)	3	2
DTR (DATA TERMINAL READY)	4	20
GND (SIGNAL GROUND)	5	7
DSR (DATA SET READY)	6	6
RTS (REQUEST TO SEND)	7	4
CTS (CLEAR TO SEND)	8	5
RI (RING INDICATOR)	9	22

2.5 UM82C862 CHIPSET PIN LAYOUT

